

Symbol-Based Successive Cancellation List Decoder for Polar Codes

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Abstract—Polar codes is promising because they can provably achieve the channel capacity while having an explicit construction method. Lots of work have been done for the bit-based decoding algorithm for polar codes. In this paper, generalized symbol-based successive cancellation (SC) and SC list decoding algorithms are discussed. A symbol-based recursive channel combination relationship is proposed to calculate the symbol-based channel transition probability. This proposed method needs less additions than the maximum-likelihood decoder used by the existing symbol-based polar decoding algorithm. In addition, a two-stage list pruning network is proposed to simplify the list pruning network for the symbol-based SC list decoding algorithm.

Index Terms—Error control codes, polar codes, successive cancellation decoding, list decoding

I. INTRODUCTION

Since polar codes were introduced by Arikan [1], they have attracted lots of interest in the fields of communication and coding theory, because they can provably achieve the channel capacity not only for arbitrary discrete memoryless channels, but also for any continuous memoryless channel [2]. However, their capacity approaching can be achieved only when the code length is large enough ($N > 2^{20}$ [3]) under the SC decoding algorithm. For short or moderate code length, in terms of the error performance, polar codes with the SC decoding algorithm is worse than turbo codes or low-density parity-check codes [4], [5].

To improve the error performance of polar codes, lots of work have been done. Systematic polar codes [6] was proposed to reduce the bit error rate while guaranteeing the same frame error rate (FER) compared with their non-systematic counterparts. An SC list decoding algorithm for polar codes was proposed in [7]. The SC list decoding algorithm outperforms the SC decoding algorithm and achieves the error performance close to that of the ML decoding algorithm at the cost of complexity of $\mathcal{O}(LN \log N)$, where L is the list size. Moreover, the concatenation of polar codes with cyclic redundancy check (CRC) codes was introduced in [4], [8]. To decode the CRC-concatenated polar codes, a CRC detector is used in the SCL decoding algorithm to help the codeword determination. The combination of an SCL decoding algorithm and a CRC detector is called CRC-aided SCL (CA-SCL) decoding algorithm. [8] shows that with the CA-SCL decoding algorithm, the error performance of a (2048, 1024) CRC-concatenated polar code is better than that of a (2304, 1152) LDPC

code, which is used in the WiMax standard [9].

To implement decoders for polar codes, several works have been done for the SC decoding algorithm. Arikan [1] showed that a fully parallel SC decoder has a latency of $2N - 1$ clock cycles. This decoder has complexity of $\mathcal{O}(N \log N)$. A tree SC decoder and a line SC decoder with complexity of $\mathcal{O}(N)$ were proposed in [10]. These two decoders have the same latency as the fully parallel SC decoder. To reduce complexity further, Leroux [3] proposed a semi-parallel SC decoder for polar codes by taking advantage of the recursive structure of polar codes to reuse processing resources. To reduce the latency, a simplified SC (SSC) polar decoder was introduced in [11] and it was further analyzed in [12]. In the SSC polar decoder, a polar code is converted to a binary tree including three types of nodes: rate-one, rate-zero and rate- R nodes. Based on the SSC polar decoder, the ML SSC decoder makes use of the ML decoding algorithm to deal with rate- R nodes in [13], [14]. However, SSC and ML-SSC polar decoders depend on positions of information bits and frozen bits, and are code-specific consequently. In [15], a pre-computation look-ahead technique was proposed to help the tree SC decoder shorten the latency by half. An efficient SCL decoder architecture was proposed in [16]. Recently, parallel decoders of polar codes were proposed in [17]. To avoid ambiguity between the aforementioned fully parallel SC decoder in [1] and parallel decoders in [17], we call the latter as symbol-based polar decoders in this paper because an M -bit symbol-based polar decoder decodes M bits at a time instead of only one bit. However, [17] is focused on some specific case and does not provide a general discussion. Meanwhile, it uses the ML decoder to calculate the symbol-based channel transition probability, which is not complexity-efficient enough.

The main contributions of this paper are:

- Generalized symbol-based polar decoding algorithms are discussed. Furthermore, a symbol-based recursive channel combination relationship is derived to calculate the symbol-based channel transition probability. The proposed method needs less additions than the ML decoder used in [17].
- An M -bit symbol-based SCL polar decoder needs to find L most-reliable lists among $2^M L$ list candidates. A two-stage list pruning network are proposed to perform this list pruning function. $2^M L$ list candidates are divided

into L groups. Each group has 2^M list candidates. In the first stage, q most-reliable lists for each group are found. Then, L most-reliable list candidates are sorted out from qL list candidates generated by the first stage. If $q < L$, the two-stage list pruning network can achieve lower complexity and a shorter critical path delay than the list pruning network with $q = L$.

The rest of our paper is organized as follows. Section II briefly reviews polar codes and existing decoding algorithms. In Section III, the generalized M -bit symbol-based SC and SCL decoding algorithms for polar codes are discussed. Based on the Arikan's recursive channel transformations, we derive the symbol-based recursive channel combination relationship to calculate the symbol-based channel transition probability. To simplify the selection of the list candidates, a two-stage list pruning network is proposed in Section IV. Some conclusions are given in Section V.

II. POLAR CODES AND EXISTING DECODING ALGORITHMS

A. Polar Codes

Polar codes are linear block codes. The block length of polar codes is restricted to a power of two, $N = 2^n$ for $n \geq 2$. We follow the notation for vectors in [1], namely $u_a^b = (u_a, u_{a+1}, \dots, u_{b-1}, u_b) = (u_a^{b-1}, u_b)$. Assume $\mathbf{u} = u_0^{N-1} = (u_0, u_1, \dots, u_{N-1})$ is the encoding bit sequence. Let $F = \begin{bmatrix} 1 & 0 \\ 1 & 1 \end{bmatrix}$. The corresponding encoded bit sequence $\mathbf{x} = x_0^{N-1} = (x_0, x_1, \dots, x_{N-1})$ is generated by

$$\mathbf{x} = \mathbf{u} B_N F^{\otimes n}, \quad (1)$$

where B_N is an $N \times N$ bit-reversal permutation matrix and $F^{\otimes n}$ denotes the n -th Kronecker power of F .

For any index set $\mathcal{A} \subset \mathcal{I} = \{0, 1, \dots, N-1\}$, let $\mathbf{u}_{\mathcal{A}}$ denote the sub-sequence of \mathbf{u} defined by $\mathbf{u}_{\mathcal{A}} = (u_i : i \in \mathcal{A})$. Denote the complement of \mathcal{A} in \mathcal{I} as \mathcal{A}^c . Let $\mathbf{u}_{\mathcal{A}^c} = (u_i : 0 \leq i < N, i \notin \mathcal{A})$. For an (N, K) polar code, the encoding bit sequence is grouped into two parts: a K -element part $\mathbf{u}_{\mathcal{A}}$ which carries information bits, and $\mathbf{u}_{\mathcal{A}^c}$ whose elements are predefined frozen bits. For the sake of convenience, frozen bits are set to be zero.

B. SC Decoding Algorithm for Polar Codes

Given a transmitted codeword \mathbf{x} and the corresponding received word \mathbf{y} , the SC decoding algorithm for an (N, K) polar code decodes the encoding bit sequence \mathbf{u} from u_0 to u_{N-1} successively one by one as shown in Alg. 1. Here, $\hat{\mathbf{u}} = (\hat{u}_0, \hat{u}_1, \dots, \hat{u}_{N-1})$ represents the estimated value for \mathbf{u} . $P(\mathbf{y}, \hat{u}_0^{j-1} | u_j)$ is the probability that \mathbf{y} is received and the previously decoded bits are \hat{u}_0^{j-1} given u_j is zero or one.

To calculate $P(\mathbf{y}, \hat{u}_0^{j-1} | u_j)$, the following Arikan's recursive

Algorithm 1: SC Decoding Algorithm [1]

```

1 for  $j = 0 : N - 1$  do
2   if  $j \in \mathcal{A}^c$  then  $\hat{u}_j = 0$  else
3     if  $\frac{P(\mathbf{y}, \hat{u}_0^{j-1} | 1)}{P(\mathbf{y}, \hat{u}_0^{j-1} | 0)} \geq 1$  then  $\hat{u}_j = 1$  else  $\hat{u}_j = 0$ 
4   end if

```

channel transformations [1] are used:

$$\begin{aligned} & P(y_0^{\Gamma-1}, u_0^{2i-1} | u_{2i}) \\ &= \frac{1}{2} \sum_{u_{2i+1}} \left[P(y_0^{\Gamma/2-1}, u_{0,e}^{2i-1} \oplus u_{0,o}^{2i-1} | u_{2i} \oplus u_{2i+1}) \right. \\ & \quad \cdot P(y_{\Gamma/2}^{\Gamma-1}, u_{0,o}^{2i-1} | u_{2i+1}) \left. \right], \end{aligned} \quad (2)$$

and

$$\begin{aligned} & P(y_0^{\Gamma-1}, u_0^{2i} | u_{2i+1}) \\ &= \frac{1}{2} P(y_0^{\Gamma/2-1}, u_{0,e}^{2i-1} \oplus u_{0,o}^{2i-1} | u_{2i} \oplus u_{2i+1}) \\ & \quad \cdot P(y_{\Gamma/2}^{\Gamma-1}, u_{0,o}^{2i-1} | u_{2i+1}), \end{aligned} \quad (3)$$

where $1 \leq \Gamma = 2^\gamma \leq N$, and $0 \leq i < \frac{N}{2}$.

C. SCL Decoding Algorithm for Polar Codes

Instead of making decision for each information bit of \mathbf{u} in an SC decoding algorithm, the SCL decoding algorithm [7] creates two paths in which the bit is assumed to be 0 and 1, respectively. If the number of paths is greater than the list size L , the L most-reliable paths are selected out. At the end of the decoding procedure, the most reliable path is chosen as $\hat{\mathbf{u}}$. The SCL decoding algorithm is described in Alg. 2. Without loss of generality, assume L to be a power of two, i.e. $L = 2^l$. Let $\mathbf{L}_i = ((\mathcal{L}_i)_0, (\mathcal{L}_i)_1, \dots, (\mathcal{L}_i)_{N-1})$ represent the i -th list vector, where $0 \leq i < L$.

Here, \mathbf{S} is a structure type array with the size of $2L$. Each element of \mathbf{S} has three members: \mathbf{P} , \mathbf{L} , and \mathbf{U} . The function `sortPDecrement` sorts the array \mathbf{S} by the decreasing order of \mathbf{P} .

D. CA-SCL Decoding Algorithm for Polar Codes

The CA-SCL decoding algorithm is used for the CRC-concatenated polar codes. The difference between the CA-SCL [8] and the SCL decoding algorithms is how to make the final decision for $\hat{\mathbf{u}}$. If there is at least one path satisfying the CRC constraint, the most-reliable CRC-valid path is chosen for $\hat{\mathbf{u}}$. Otherwise, the decision rule of the SCL decoding algorithm is used for the CA-SCL decoding algorithm. Since now, without being specified, polar codes mentioned in the following sections are CRC-concatenated polar codes.

III. M-BIT SYMBOL-BASED DECODING ALGORITHM FOR POLAR CODES

A. Generalized Symbol-based SC Decoding Algorithm for Polar Codes

In [17], only two-bit, four-bit, eight-bit symbol-based decoding algorithm for polar codes are discussed. Here, a

Algorithm 2: SCL Decoding Algorithm [7]

```

1  $\alpha = 1;$ 
2 for  $j = 0 : N - 1$  do
3   if  $j \in \mathcal{A}^c$  then
4     for  $i = 0 : \alpha - 1$  do
5        $(\mathcal{L}_i)_j = 0;$ 
6   else if  $2\alpha \leq L$  then
7     for  $i = 0 : \alpha - 1$  do
8        $(\mathcal{L}_i)_0^j = ((\mathcal{L}_i)_0^{j-1}, 0);$ 
9        $(\mathcal{L}_{i+\alpha})_0^j = ((\mathcal{L}_i)_0^{j-1}, 1);$ 
10     $\alpha = 2\alpha;$ 
11  else
12    for  $i = 0 : L - 1$  do
13       $S[i].P = P(\mathbf{y}, (\mathcal{L}_i)_0^{j-1} | 0);$ 
14       $S[i].L = (\mathcal{L}_i)_0^{j-1};$ 
15       $S[i].U = 0;$ 
16       $S[i+L].P = P(\mathbf{y}, (\mathcal{L}_i)_0^{j-1} | 1);$ 
17       $S[i+L].L = (\mathcal{L}_i)_0^{j-1};$ 
18       $S[i+L].U = 1;$ 
19     $\text{sortPDecrement}(\mathbf{S});$ 
20    for  $i = 0 : L - 1$  do
21       $(\mathcal{L}_i)_0^j = (S[i].L, S[i].U);$ 
22     $\alpha = L;$ 
23  $\hat{\mathbf{u}} = \mathbf{L}_0;$ 

```

generalized M -bit symbol-based decoding algorithm for polar codes is discussed. Without loss of generality, assume M is a power of two, i.e. $M = 2^m$ ($0 \leq m \leq n$). Define $\mathcal{IM}_j \stackrel{\text{def}}{=} \{jM, jM+1, \dots, jM+M-1\} \subset \mathcal{I}$, for $0 \leq j < \frac{N}{M}$. \mathcal{AM}_j and \mathcal{AM}_j^c are defined as:

$$\mathcal{AM}_j \stackrel{\text{def}}{=} \mathcal{IM}_j \cap \mathcal{A} \quad \text{and} \quad \mathcal{AM}_j^c \stackrel{\text{def}}{=} \mathcal{IM}_j \cap \mathcal{A}^c. \quad (4)$$

Then the decision rule of the M -bit symbol-based SC decoding algorithm can be described as,

$$\hat{u}_{jM}^{jM+M-1} = \arg \max_{\substack{u_{\mathcal{AM}_j} \in \{0,1\}^{|\mathcal{AM}_j|} \\ u_{\mathcal{AM}_j^c} \in \{0\}^{|\mathcal{AM}_j^c|}}} P(\mathbf{y}, \hat{u}_0^{jM-1} | u_{jM}^{jM+M-1}), \quad (5)$$

where $|\mathcal{AM}_j|$ represents the cardinality of \mathcal{AM}_j . If $M = N$, this decoding algorithm is a maximum-likelihood sequence decoding algorithm.

If all bits of \mathbf{u} are independent and each bit has an equal probability of being a 0 or 1, the following symbol-based recursive channel combination relationship can be used to calculate the symbol-based channel transition probability $P(\mathbf{y}, u_0^{jM-1} | u_{jM}^{jM+M-1})$:

Proposition 1. For any $0 \leq m \leq n$, $N = 2^n$, $M = 2^m$, $0 \leq j < \frac{N}{M}$, assume $v_0^{N-1} \stackrel{\text{def}}{=} u_{0,e}^{2N-1} \oplus u_{0,o}^{2N-1}$ and $v_N^{2N-1} \stackrel{\text{def}}{=}$

$u_{0,o}^{2N-1}$, then

$$P(y_0^{2N-1}, u_0^{2jM-1} | u_{2jM}^{2jM+2M-1}) = P(y_0^{N-1}, v_0^{jM-1} | v_{jM}^{jM+M-1}) \cdot P(y_N^{2N-1}, v_N^{N+jM-1} | v_{N+jM}^{N+jM+M-1}) \quad (6)$$

Proof: According to Bayes' theorem,

$$P(y_0^{2N-1}, u_0^{2jM-1} | u_{2jM}^{2jM+2M-1}) = \frac{P(y_0^{2N-1}, u_0^{2jM+2M-2} | u_{2jM+2M-1})}{P(u_{2jM}^{2jM+2M-2} | u_{2jM+2M-1})}. \quad (7)$$

Because all bits of \mathbf{u} are independent and each bit has an equal probability of being a 0 or 1,

$$P(u_{2jM}^{2jM+2M-2} | u_{2jM+2M-1}) = P(u_{2jM}^{2jM+2M-2}) = P(u_{2jM})P(u_{2jM}) \cdots P(u_{2jM+2M-2}) = 2^{-(2M-1)}.$$

Therefore,

$$P(y_0^{2N-1}, u_0^{2jM-1} | u_{2jM}^{2jM+2M-1}) = 2^{(2M-1)} P(y_0^{2N-1}, u_0^{2jM+2M-2} | u_{2jM+2M-1}). \quad (8)$$

According to Eq. (3),

$$P(y_0^{2N-1}, u_0^{2jM+2M-2} | u_{2jM+2M-1}) = \frac{1}{2} P(y_0^{N-1}, v_0^{jM+M-2} | v_{jM+M-1}) \cdot P(y_N^{2N-1}, v_N^{N+jM+M-2} | v_{N+jM+M-1}). \quad (9)$$

According to the definition of v_0^{N-1} , all bits of v_0^{N-1} are independent and $P(v_j = 0) = P(v_j = 1) = \frac{1}{2}$ for $0 \leq j < N$. Then we have

$$P(y_0^{N-1}, v_0^{jM+M-2} | v_{jM+M-1}) = 2^{-(M-1)} P(y_0^{N-1}, v_0^{jM-1} | v_{jM}^{jM+M-1}). \quad (10)$$

Similarly,

$$P(y_N^{2N-1}, v_N^{N+jM+M-2} | v_{N+jM+M-1}) = 2^{-(M-1)} P(y_N^{2N-1}, v_N^{N+jM-1} | v_{N+jM}^{N+jM+M-1}). \quad (11)$$

Then, by equations (8) ~ (11), Eq. (6) is obtained. ■

Similar to the SC decoding algorithm, an M -bit symbol-based SC decoding algorithm can be represented by using a message flow graph (MFG) as well, where a channel transition probability is referred as a *message* for the sake of convenience. If the code length of a polar code is N , the MFG can be divided into $(n+1)$ stages S_0, S_1, \dots, S_n : one initial stage S_0 and n calculation stages. For the SC decoding algorithm, all calculation stages carry out the calculation of Eq. (2) and (3). However, for the M -bit symbol-based SC decoding algorithm, the Arikan's recursive transformations are performed in the first $(n-m)$ calculation stages, called channel transformation stages. In the last m calculation stages, called channel combination stages, Eq. (6) is used to compute messages. Therefore, an M -bit symbol-based SC decoding algorithm contains two parts. The first part contains calculations of the first $n-m$ stages and consists of M SC decoders for polar codes of length $\frac{N}{M}$. These SC decoders are called as component decoders.

There are no message exchange between these component decoders. Channel combination stages use outputs of channel transformation stages to calculate symbol-based messages and feed the estimated symbol back to component decoders to update partial-sums.

For example, as shown in Fig. 1, the MFG of a four-bit symbol-based SC decoding algorithm for a polar code with $L = 8$ has four stages. Messages of the initial stage (S0) come from the channel directly. Messages of the first stage (S1) are calculated with Arikan's recursive transformations. Messages of the second and third stages (S2 and S3) are calculated with Eq. (6). The four small gray boxes on the right are four component SC decoders. And stages in the big gray box on the left are channel combination stages. Here,

$$\begin{aligned} v_0^3 &= u_{0,e}^7 \oplus u_{0,o}^7, & v_4^7 &= u_{0,o}^7, \\ w_0 &= v_0 \oplus v_1 = u_0 \oplus u_1 \oplus u_2 \oplus u_3, \\ w_1 &= v_2 \oplus v_3 = u_4 \oplus u_5 \oplus u_6 \oplus u_7, \\ w_2 &= v_1 = u_2 \oplus u_3, \\ w_3 &= v_3 = u_6 \oplus u_7, \\ w_4 &= v_4 \oplus v_5 = u_1 \oplus u_3, \\ w_5 &= v_6 \oplus v_7 = u_5 \oplus u_7, \\ w_6 &= v_5 = u_3, \\ w_7 &= v_7 = u_7. \end{aligned}$$

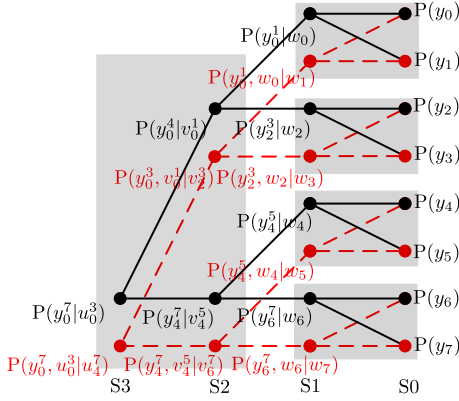


Fig. 1. The message flow graph of a four-bit symbol-based SC decoding algorithm for a polar code with a length of eight.

We can take advantage of the symbol-based channel combination to reduce complexity of calculating the symbol-based channel transition probability. In [17], an ML decoder is used to calculate the symbol-based message of stage S_n from output of component decoders directly. There are 2^M possible values for an M -bit symbol. [13] shows that $(M-1)$ additions are needed to calculate the log-likelihood (LL) message corresponding to each value. Therefore, an ML decoder needs $2^M(M-1)$ additions in total. In channel combination stages, there are 2^{n-i} nodes in the i -th stage and each node contains 2^{M+i-n} messages. One addition is needed to compute each LL message according to Eq. (6). Hence, channel combination stages need $\sum_{i=0}^{m-1} 2^i 2^{\frac{M}{2^i}}$ additions in total. For the example shown

in Fig. 1, the ML decoder needs $2^4(4-1) = 48$ additions. The channel combination stages need only $2^4 + 2 \times 2^2 = 24$ additions, which is only a half of those needed by the ML decoder.

In terms of the error performance, simulations of [17] show that there is no observed performance loss for the the M -bit symbol-based SC decoding algorithm using the ML decoder to calculate the symbol-based message, compared with the SC polar decoding algorithm. Since our channel combination relationship can be used to provide the same calculation results as the ML decoder used in [17] does, the M -bit symbol-based SC decoding algorithm using the symbol-based channel combination relationship does not have any observed performance degradation compared with the SC decoding algorithm.

B. Generalized Symbol-based SCL Decoding Algorithm for Polar Codes

The symbol-based SCL decoding algorithm is more complex than the SCL algorithm, since the path expansion coefficient is not a constant any more. In the SCL algorithm, for each information bit, the path expansion coefficient is two. But for the M -bit symbol-based SCL decoding algorithm, the path expansion coefficient is $2^{|\mathcal{A}_{M_j}|}$, which depends on the number of information bits in an M -bit symbol. The M -bit symbol-based SCL decoding algorithm is described in Alg. 3.

Here, without any ambiguity, $\mathbf{0}$ represents a zero vector whose bit-width is determined by the left-hand operator. The function $\text{dec2bin}(d, b)$ converts a decimal number d to a b -bit binary vector. Eq. (6) can also be used to calculate the symbol-based channel transition probability corresponding to each list, i.e. $P(\mathbf{y}, (\mathcal{L}_i)_{0}^{jM-1} | u_{jM}^{jM+M-1})$.

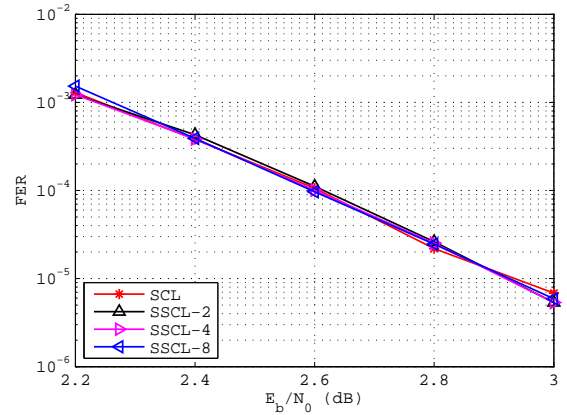


Fig. 2. FERs of symbol-based SCL decoding algorithms for a (1024, 512) polar code with $L = 4$.

Fig. 2 shows FERs of symbol-based SCL decoding algorithms for a (1024, 512) polar code with $L = 4$. Performance differences between these curves are very minor. Therefore, by applying Eq. (6) the symbol-based SCL algorithm does not introduce the obvious performance loss compared with the SCL decoding algorithm. Even with different M s, these

Algorithm 3: M -bit Symbol-based SCL Decoding Algorithm [17]

```

1  $\alpha = 1$ ;
2 for  $j = 0 : \frac{N}{M} - 1$  do
3    $\beta = 2^{\lfloor \mathcal{AM}_j \rfloor}$ ;
4   if  $\beta == 1$  then
5     for  $i = 0 : \alpha - 1$  do
6        $(\mathcal{L}_i)_{jM}^{jM+M-1} = \mathbf{0}$ ;
7   else if  $\alpha\beta \leq L$  then
8      $u_{\mathcal{AM}_j} = \mathbf{0}$ ;
9     for  $k = 0 : \beta - 1$  do
10       $u_{\mathcal{AM}_j} = \text{dec2bin}(k, \lfloor \mathcal{AM}_j \rfloor)$ ;
11      for  $i = 0 : \alpha - 1$  do
12         $t = i + k\alpha$ ;
13         $(\mathcal{L}_t)_0^{jM+M-1} = ((\mathcal{L}_i)_0^{jM-1}, u_{jM}^{jM+M-1})$ ;
14       $\alpha = \alpha\beta$ ;
15   else
16      $u_{\mathcal{AM}_j} = \mathbf{0}$ ;
17     for  $k = 0 : \beta - 1$  do
18       $u_{\mathcal{AM}_j} = \text{dec2bin}(k, \lfloor \mathcal{AM}_j \rfloor)$ ;
19      for  $i = 0 : L - 1$  do
20         $t = i + kL$ ;
21         $S[t].P = P(\mathbf{y}, (\mathcal{L}_i)_0^{jM-1} | u_{jM}^{jM+M-1})$ ;
22         $S[t].L = (\mathcal{L}_i)_0^{jM-1}$ ;
23         $S[t].U = u_{jM}^{jM+M-1}$ ;
24       $\text{sortPDecrement}(S)$ ;
25      for  $i = 0 : L - 1$  do
26         $(\mathcal{L}_i)_0^{jM+M-1} = (S[i].L, S[i].U)$ ;
27       $\alpha = L$ ;

```

performance curves are very close. Here, SSCL- i denotes the i -bit symbol-based SCL decoding algorithm.

IV. TWO-STAGE LIST PRUNING NETWORK

For the M -bit symbol-based SCL decoding algorithm, the maximum path expansion coefficient is 2^M , i.e. each existing list generates 2^M list candidates. Therefore, in the worst-case scenario, L most-reliable lists should be sorted out of $2^M L$ list candidates. To facilitate this sorting network, we propose a two-stage list pruning network. In the first stage, q most-reliable lists are found out among 2^M list candidates of each existing list. Therefore, there are qL list candidates left. In the second stage, the L most-reliable lists are sorted out from the qL list candidates generate by the first stage. The message flow of a two-stage list pruning network is illustrated in Fig. 3.

It is easy to prove that if $q \geq L$ and $2^M > L$, the L lists found by the two-stage list pruning network are exactly the L most-reliable lists among the $2^M L$ list candidates. Therefore, we only consider $q \leq L$. In terms of complexity, a smaller q leads to a two-stage list pruning network with lower

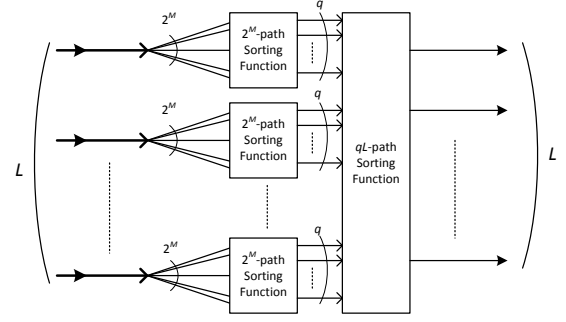


Fig. 3. Message flow for a two-stage list pruning network.

complexity but the probability that the L lists found by the two-stage list pruning network are exactly the L most-reliable lists among the $2^M L$ list candidates decreases as well. This may cause some performance loss.

Fig. 4 and 5 show how different qs affect FERs of an SSCL-8 decoding algorithm for a (1024, 512) polar code with $L = 4$ and $L = 16$, respectively. When $L = 4$ and $q = 2$, the SSCL-8 decoding algorithm shows an FER performance loss of about 0.2 dB. When $L = 16$, compared with the FER performance with $q = 16$, there is no observed performance degradation when $q = 8$. The performance loss due to $q = 4$ is about 0.08 dB. Therefore, for $L = 16$, to reduce complexity and the latency of the two-stage list pruning network, q can be 8. If the 0.08 dB performance loss is tolerated, q can be reduced further to four.

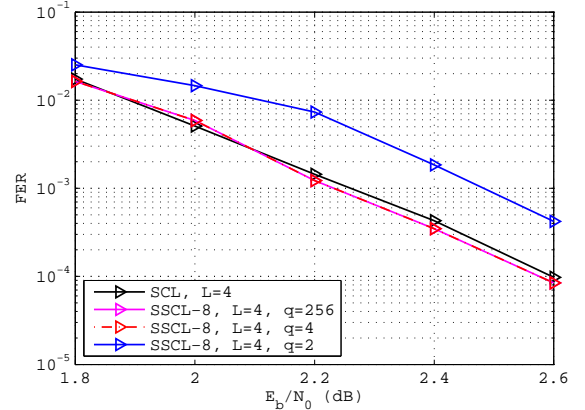


Fig. 4. FERs of SSCL-8 decoder for a (1024, 512) polar code with $L = 4$.

Similarly, as shown in Fig. 6, for a (2048,1433) polar code, the two stage list-pruning network of $q = 4$ helps to reduce the complexity of SSCL-8 decoder without the obvious performance loss.

Fig. 7 shows FERs of an SSCL-4 decoder for a (1024,512) polar code with $L = 8$ while different qs are used. Compared with the case of $q = 8$, there is no obvious FER performance loss when $q = 4$. However, $q = 2$ incurs an FER performance loss of about 0.3 dB when the FER is 10^{-3} .

To illustrate advantages of two-stage list pruning network, two tree sorting networks are designed to find the 8 maximal

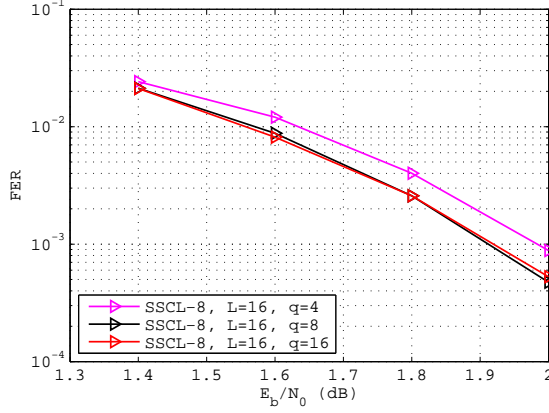


Fig. 5. FERs of SSCL-8 decoder for a (1024, 512) polar code with $L = 16$.

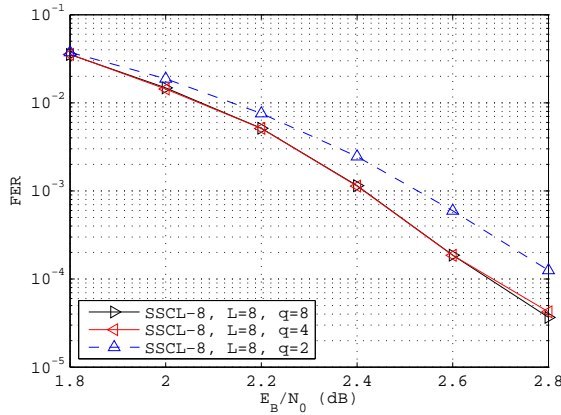


Fig. 6. FERs of SSCL-8 decoding algorithm for a (2048, 1433) polar code with $L = 8$.

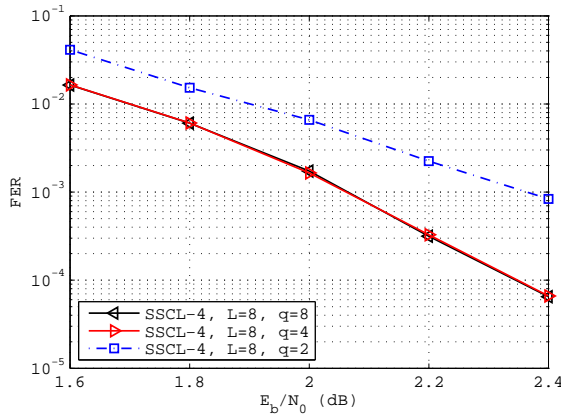


Fig. 7. FERs of SSCL-4 decoder for a (1024, 512) polar code with $L = 8$.

values out of 128 values which can be used in the SSCL-4 decoder with $L = 8$. One is a conventional tree sorting network, shown in Fig. 8, referred to as CTSN. The other is a two-stage tree sorting network with $q = 4$, shown in Fig. 9, referred to as TSTSN. Here, the "ps16to8" block is a bitonic sorter which finds the maximal 8 values out of 16 values. The bitonic sorter, "ps8to4", finds the maximal 4 values out of 8 values.

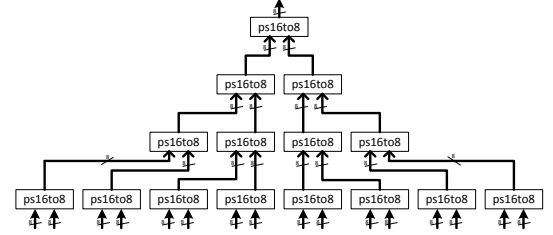


Fig. 8. A conventional tree sorting network to find the 8 maximal values out of 128 values.

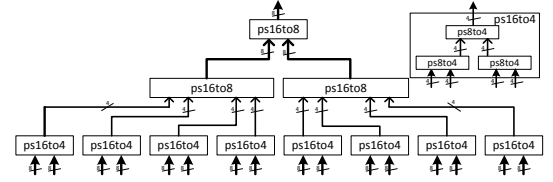


Fig. 9. A two-stage tree sorting network to find the 8 maximal values out of 128 values.

We implement these two sorting networks and use the RTL compiler to synthesize them with a TSMC 90-nm CMOS technology. The TSTSN has a smaller area and a shorter critical path than the CTSN, as shown in Table I. Besides, the TSTSN does not introduce any obvious performance degradation as shown in Fig. 7.

TABLE I
SYNTHESIZING RESULTS FOR CTSN AND TSTSN.

Design	area (mm ²)	Critical Path Delay (ns)
CTSN	0.206	7.463
TSTSN	0.134	5.861

V. CONCLUSION

In this paper, we discuss the generalized symbol-based SC and SCL decoding algorithm for polar codes and derive the recursive procedure to calculate the symbol-based channel transition probability. This recursive procedure needs less additions than the ML scheme used in [17]. A two-stage list pruning network is also proposed to simplify the L -list finding network.

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